L Number	Hits	Search Text	DB	Time stamp
1	30247	sense adj amplifier	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:49
2	9933	Cell adj area	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:49
3	404	I/O adj area	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:49
4	12444	peripheral adj area	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:50
5	18	(sense adj amplifier) and (peripheral adj area) and (Cell adj area)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:53
6	705	open adj bit adj line	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:54
7	496	(sense adj amplifier) and (open adj bit adj line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:54
8	118	((sense adj amplifier) and (open adj bit adj line)) and (Cell adj area)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 18:15
9	9226	(sense adj amplifier) and DRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 16:59
10	46373	bit adj line	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:00
11	6125	((sense adj amplifier) and DRAM) and (bit adj line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:01
12	3779	(((sense adj amplifier) and DRAM) and (bit adj line)) and area	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:02

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13	1921	((((sense adj amplifier) and DRAM) and (bit adj line)) and area) and layout	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:02
14	964	(((((sense adj amplifier) and DRAM) and (bit adj line)) and area) and layout) and cell and peripheral	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:03
15	980	open adj bit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:03
16	116	((((((sense adj amplifier) and DRAM) and (bit adj line)) and area) and layout) and cell and peripheral) and (open adj bit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:19
17	0	(thinh adj nguyen).XA.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:21
18	28707	(thinh nguyen).XA.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:22
19	1582	(hoai ho).XP.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:23
20	46	((thinh nguyen).XA.) and ((hoai ho).XP.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:50
21	18	"5602772"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 17:50
22	1356	second adj memory near area	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 18:16
23	357	sense adj amplifier near area	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 18:17
24	2	(second adj memory near area) and (sense adj amplifier near area)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/13 18:19

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| 25 | 93818 | second adj layer | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/08/13 18:19 |
| 26 | 1 | ((second adj memory near area) and (sense adj amplifier near area)) and (second adj layer) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/08/13 18:19 |

Patent Assignment Abstract of Title

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Invent rs: Kiyoshi Nakai, Hidetoshi Iwai

Title: Semiconductor integrated circuit device

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